# August 2001

IGBT

# SGR20N40L / SGU20N40L

# **General Description**

**FAIRCHILD** 

Insulated Gate Bipolar Transistors (IGBTs) with a trench gate structure provide superior conduction and switching performance in comparison with transistors having a planar gate structure. They also have wide noise immunity. These devices are very suitable for strobe applications

### Features

- High input impedance
- High peak current capability (150A)
- Easy gate drive
- Surface Mount : SGR20N40L
- Straight Lead : SGU20N40L

# Application

Strobe flash.



# Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Description	SGR / SGU20N40L	Units	
V <sub>CES</sub>	Collector - Emitter Voltage	400	V	
V <sub>GES</sub>	Gate - Emitter Voltage	± 6	V	
СМ (1)	Pulsed Collector Current	150	А	
P <sub>C</sub>	Maximum Power Dissipation @ T <sub>C</sub> = 25	°C 45	W	
TJ	Operating Junction Temperature	-40 to +150	°C	
T <sub>stg</sub>	Storage Temperature Range	-40 to +150	°C	
TL	Maximum Lead Temp. for soldering purposes, 1/8" from case for 5 seconds	300	°C	

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

# **Thermal Characteristics**

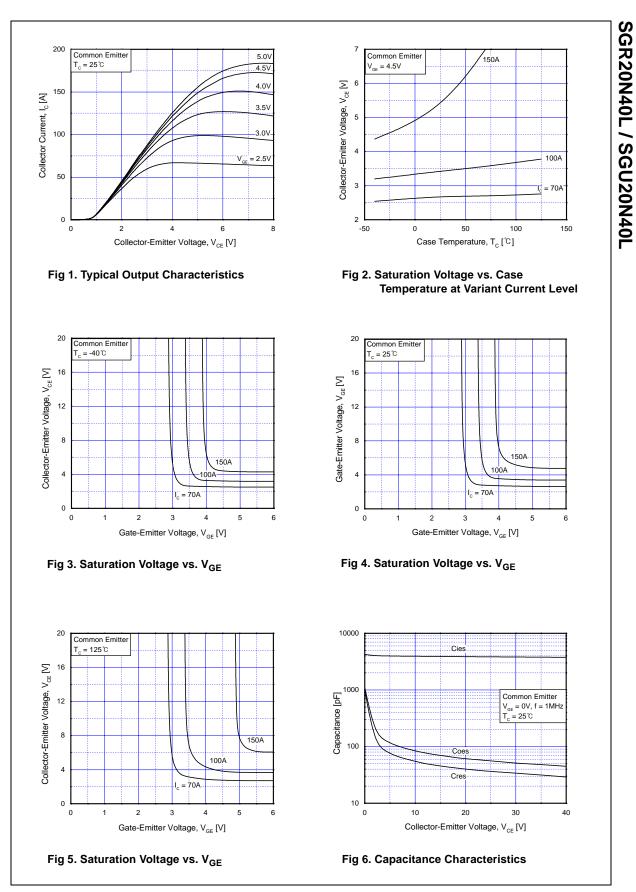
Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		3.0	°C/W
R <sub>0JA</sub> (D-PAK)	Thermal Resistance, Junction-to-Ambient (PCB Mount) (2)		50	°C/W
R <sub>θJA</sub> (I-PAK)	Thermal Resistance, Junction-to-Ambient		110	°C/W

Notes :

(2) Mounted on 1" square PCB (FR4 or G-10 Material)

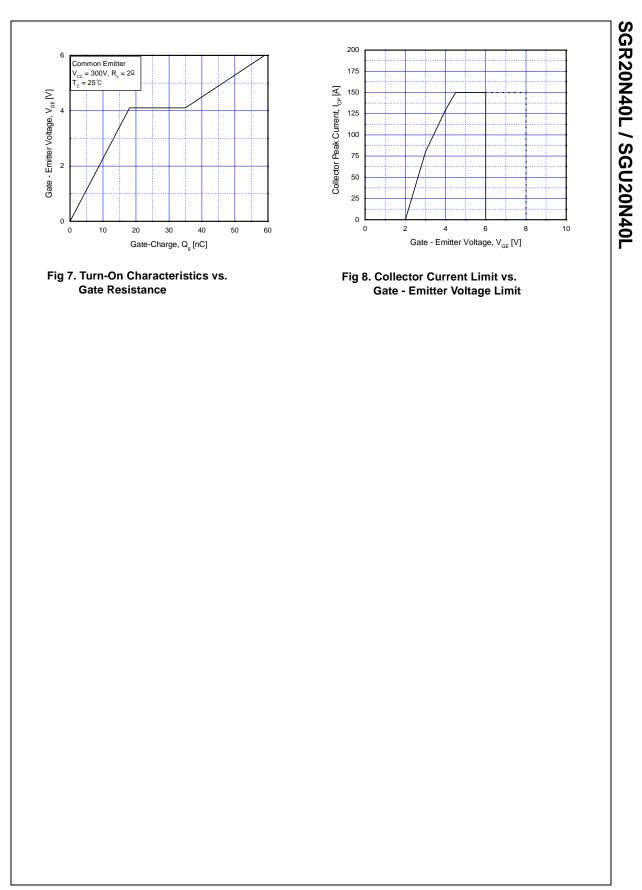
off Char	Parameter	Test Conditions	Min.	Тур.	Max.	Units
un char	acteristics					
BV <sub>CES</sub>	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$	450			V
CES	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			10	μA
GES	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			±0.1	μΑ
On Char	acteristics					
V <sub>GE(th)</sub>	G-E Threshold Voltage	$I_C = 1 \text{mA}, V_{CE} = V_{GE}$	0.5	1.0	1.4	V
V <sub>CE(sat)</sub>	C-E Saturation Current	$I_{\rm C} = 150$ A, $V_{\rm GE} = 4.5$ V	2.0	4.5	8.0	V
	Input Capacitance Output Capacitance	$V_{GE} = 0V, V_{CE} = 30V,$		3800 50		pF pF
C <sub>oes</sub>	Output Capacitance			50		pF
Cres	Reverse Transfer Capacitance	f = 1MHz		35		pF
d <u>(on)</u> r	ng Characteristics Turn-On Delay Time Rise Time	$V_{CC} = 300V, I_C = 150A,$ $V_{GE} = 4.5V, R_G = 15\Omega^*$		0.2		μs μs
t <sub>d(off)</sub>	Turn-Off Delay Time	- Resistive Load		0.3	0.5	μs
t <sub>f</sub>	Fall Time			1.5	2.0	μs

SGR20N40L / SGU20N40L



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